tion no. 09/837,007

May 20, 2002

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Attn:

Art Unit 2827 - Examiner James Mitchell

commissioner of Patents and Trademarks

FROM:

MEG01-004

George O. Saile, Reg. No. 19,572

20 McIntosh Drive

Poughkeepsie, N.Y. 12603

Washington, D.C. 20231

SUBJECT:

Serial #:

09/837,007

File Date:

April 18, 2002 M.S. Lin, et al.

Inventor: Examiner:

James Mitchell

Art Unit:

2827

Title:

A Structure and Manufacturing Method of a Chip Scale

Package

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

This is in response to the Restriction Requirement in the Office Action dated April 18, 2002. In that office action, restriction was required to one of the following Inventions

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on May 20, 2002.

> Signature Stephen B. Ackerman, Reg. No. 37,761

Date: May 20, 2002

under 35 U.S.C.³⁴121:

The inventions stated are:

I - Claims 13-24, drawn to a device, classified in class 257, subclass 738, and

II - Claims 1-12, drawn to a method, classified in class 438, subclass 613.

Applicant provisionally elects to be examined the Invention described by the Examiner as Group I - Claims 13-24, drawn to a device classified in Class 257, subclass 738. This election is made with traverse of the requirement under 37 C.F.R. 1.143 for the reasons given in the following paragraphs.

The Examiner is respectfully requested to reconsider the Requirement for Restriction in the Office Action.

The Examiner gives the reasons for the distinctness between Inventions I and II as (1) that the process as claimed can be used to make other and materially different product, or (2) the product as claimed can be made by another and materially different process (M.P.E.P. 806.05(f)). However, upon reading the process Claims against the product Claims one can readily see that the process Claims are directed to a method of forming "creating a semiconductor device package", and that the product claims are directed to "a semiconductor device package", and that it is necessary to obtain claims in

both the process and product claim language. The process claims necessarily use the product and vice versa. The field of search must necessarily cover both the process class/subclass 438/613 and product class/subclass 257/738, in addition to other related classes and subclasses, to provide a complete and adequate search. The fields of search for Groups I and II are clearly and necessarily co-extensive. The Examiner's suggestion that "the product as claimed can be made by another and materially different process such as without a step of cleaning flux" is speculative and has nothing to do with the Claims as presented in this patent application.

Further, it is respectfully suggested that these reasons are insufficient to place the additional cost of a second patent application upon the applicants. Therefore, it is respectfully requested that the Examiner withdraw this restriction requirement for these reasons.

Withdrawal of the Restriction Requirement, and allowance of the present Patent Application, is respectfully requested.

Respectfully submitted,

Stephen B. Ackerman, Reg. No, 37,761